



JAYPEE UNIVERSITY OF INFORMATION TECHNOLOGY
WAKNAGHAT, P.O. – WAKNAGHAT,
TEHSIL – KANDAGHAT, DISTRICT – SOLAN (H.P.)
PIN – 173234 (INDIA) Phone Number- +91-1792-257999
(Established by H.P. State Legislature vide Act No. 14 of 2002)



Date: 16.05.2024

**Orientation Lecture/ Outreach and End semester Viva/ Industrial Project evaluation
at Atal Bihari Vajpayee Govt. Institute of Engineering and Technology (ABVGIET),
(H.P.) on 16/05/2024**

Outreach Activity Report: Atal Bihari Vajpayee Govt. Institute of Engineering and Technology
(ABVGIET), (H.P.)

Date: 16/05/2024

Location: ABVGIET, Pragatinagar (H.P.)

Prepared by: Dr. Harsh Sohal, ECE Department, JUIT

This report outlines the outreach activities conducted at [University Name] aimed at enhancing student awareness and understanding of career prospects in the fields of VLSI (Very Large Scale Integration) and ECE (Electronics and Communication Engineering). The activities included informational sessions on career opportunities, an evaluation of final year students' projects through viva assessments, and a productive meeting with the university director.

This visit focused on the following objectives:

1. To inform engineering students about career prospects in the VLSI and ECE sectors.
2. To assess the final year students' projects through viva evaluations, providing constructive feedback.
3. To strengthen relationships and collaboration between the university and industry through discussions with the university leadership.

Activities Conducted

A Career Prospects Presentation was conducted by the team including Dr. Harsh Sohal, Dr. Vikas Baghel and Dr. Nishant Jain in the conference hall, ABVGIET, Pragatinagar (H.P.). The presentation started at 10:00 am and was attended by Final years students and Faculty members. The presentation introduced the students about:

- Overview of the VLSI industry and emerging trends.



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- Career paths available within ECE, including roles in design, research, and development.
 - Projections on future hiring trends and skill requirements in the industry

The talk concluded with an interactive Q&A session to address students' queries regarding internships, job placements, and industry preparedness.

This followed the Viva / Project Evaluation of Final Year Projects was conducted from 11:30 am in 3 parallel batches. Students were divided in groups of 10 each for 3 different batches.

- Participated in the evaluation of 30 final year student projects.
- Provided constructive feedback on project execution and presentation skills.
- Highlighted innovative approaches and commendable teamwork.
- Discussed potential real-world applications of various projects.

The evaluation was followed with the meeting with the Prof.(Dr.) Umesh C. Rathore, Principal/Director of ABVGIET, Pragatinagar (H.P.). The meeting was attended by Er. Sandeep Thakur, HOD, ECE, Dr. Harsh Sohal, Dr. Vikas Baghel and Dr. Nishant Jain. In the meeting we discussed the alignment of curriculum with industry needs and emerging technologies. He also shared his experiences and efforts he has been making to explore partnership opportunities for internships, workshops, and placements for students. All the attendees agreed to continue the regular interactions to facilitate ongoing communication between the institutions.

The outreach activities at ABVGIET, Pragatinagar (H.P.) successfully engaged students and faculty in meaningful discussions about career prospects in VLSI and ECE. The viva evaluations provided valuable insights into student projects, while the meeting with Prof.(Dr.) Umesh C. Rathore, Principal/Director of ABVGIET, Pragatinagar (H.P.) paved the way for fruitful collaborations in the future. Some pictures of the visit are given below:



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